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feedforward adj amplifier and second adj control

			Document ID	Issue Date	Pages	Title	Current OR	Current Xref	Retrieval Detail	Inventor						
1			US 2005029918 A1	20050217	33	Predistortion circuit, low-distortion power amplifier, and control methods therefor	330/149			Matsuura, Toru et al.						
2			US 20040299440 A1	20041202	24	System and method for adjusting group delay	333/156			Gurvich, Mark et al.						
3			US 20040178048 A1	20040916	22	System and method for adjusting group delay	330/151			Gurvich, Mark et al.						
4			US 20040135631 A1	20040715	31	Predistortion circuit, low-distortion power amplifier, and control methods therefor	330/149			Matsuura, Toru et al.						
5			US 20040124073 A1	20040603	7	Feed-forward back suppressed noise circuits	330/149			Ascomura, Franklin G. et al.						
6			US 20050159110 A1	20030911	31	Predistortion circuit, low-distortion power amplifier, and control methods therefor	330/149			Matsuura, Toru et al.						
7			US 20030042079 A1	20030306	24	System and method for adjusting group delay	330/151	333/217		Gurvich, Mark et al.						
8			US 2001006340 A1	20011206	31	Predistortion circuit, low-distortion power amplifier, and control methods therefor	330/149			Matsuura, Toru et al.						
9			US 6850219 B2	20050215	22	System and method for adjusting group delay	330/150	330/150 330/144 330/141		Gurvich, Mark et al.						
10			US 6819173 B2	20041116	12	Method and apparatus for reduction of distortion in a transmitter	330/149	330/151		Louis, Edward Vincent et al.						
11			US 6744316 B2	20040601	15	Method and apparatus for reduction of distortion in a transmitter	330/151	330/149		Louis, Edward Vincent et al.						

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